



TECHNOLOGICAL EDUCATIONAL  
INSTITUTE (T.E.I.) OF ATHENS  
  
FACULTY OF  
TECHNOLOGICAL APPLICATIONS  
DEPARTMENT OF ELECTRONICS



## DIPLOMA SUPPLEMENT

*This Diploma Supplement model was developed by the European Commission, Council of Europe and UNESCO/CEPES. The purpose of the supplement is to provide sufficient independent data to improve the international 'transparency' and fair academic and professional recognition of qualifications (diplomas, degrees, certificates etc.). It is designed to provide a description of the nature, level, context, content and status of the studies that were pursued and successfully completed by the individual named on the original qualification to which this supplement is appended. It should be free from any value judgments, equivalence statements or suggestions about recognition. Information in all eight sections should be provided. Where information is not provided, an explanation should give the reason why.*

### 1. INFORMATION IDENTIFYING THE HOLDER OF THE QUALIFICATION

- 1.1 Family name(s):
- 1.2 Given name(s):
- 1.3 Date of birth (*day/month/year*):
- 1.3.1 Place of birth:
- 1.3.2 Country:
- 1.4 Student identification code or number (*if available*):

### 2. INFORMATION IDENTIFYING THE QUALIFICATION

- 2.1 Name of qualification and (*if applicable*) title conferred (*in original language*):  
Ptychio (Degree)
- 2.2 Main field(s) of study for the qualification:  
Electronics Engineering
- 2.3 Name and status of awarding institution (*in original language*):  
Technologiko Ekpedeytiko Idrima (T.E.I.) Athens, a state institution of Higher Education
- 2.4 Name and status of institution (*if different from 2.3*) administering studies (*in original language*):  
As above.
- 2.5 Language(s) of instruction/examination:  
Greek

### 3. INFORMATION ON THE LEVEL OF THE QUALIFICATION

**3.1 Level of qualification:**

Undergraduate (240 ECTS)

**3.2 Official length of programme:**

Duration in years: 4 years (8 semesters)  
 Teaching Weeks per semester: 13 (thirteen)  
 ECTS Course Credits: 240  
 Workload (WL): 7.030 hours  
 Training Placement: 6 months at the 8<sup>th</sup> semester of studies

**3.3 Access requirements:**

Certificate of Upper Secondary Education (Lyceum) and (Panhellenic) entrance examinations.

**4. INFORMATION ON THE CONTENTS AND RESULTS GAINED****4.1 Mode of study:**

Full-time

**4.2 Programme requirements:**

Students receive their degree when:

- (i) they have successfully completed their compulsory courses, mandatory electives, as well as any optional courses of the undergraduate curriculum;
- (ii) their graduation project (dissertation) has been successfully approved completed and examined;
- (iii) they have successfully completed their Industrial Placement (practical training);
- (iv) they have completed four (4) academic years of study, and have accumulated 240 ECTS credits from (i), (ii) and (iii) above.

**4.3 Programme details: (e.g. modules or units studied), and the individual grades/marks/credits obtained:****CORE MODULES (C)**

No.	Course ID	Course Title	Semester	ECTS Course Credits	Grades
1	EE-1012	Physics	A'	7	
2	EE-1022	Semiconductor Physics	A'	7	
3	EE-3031	Electromagnetism	C'	4	
4	EE-1031	Mathematics	A'	6	
5	EE-1052	Computer Programming I	A'	4	
6	EE-2021	Applied Mathematics	B'	6	
7	EE-2042	Computer Programming II	B'	6	
8	EE-2052	Physics of Sensors	B'	5	
9	EE-3061	Special Topics of Applied Mathematics	C'	4	
10	EE-4031	Signals and Systems	D'	4,5	
11	EE-4062	Modern Operating Systems	D'	4	
		<b>Total</b>		<b>57,50</b>	

**Modules of ADMINISTRATION, ECONOMICS, LEGISLATION AND HUMANITIES  
(AELH)**

No.	Course ID	Course Title	Semester	ECTS Course Credits	Grades
12	EE-4051	Project Management	D'	4	
13	EE-7051	Electronic Commerce – Entrepreneurship	G'	3	
		<b>Total</b>		<b>7,00</b>	

**SPECIAL MODULES (S)**

No.	Course ID	Course Title	Semester	ECTS Course Credits	Grades
1	EE-1041	Electric Circuits	A'	6	
2	EE-2012	Analogue Electronics I	B'	8	
3	EE-3012	Analogue Electronics II	C'	7	
4	EE-4042	Analogue Electronics III	D'	7,5	
5	EE-2032	Computer aided Electronic Design – Electronic Materials	B'	5	
6	EE-3022	Digital Electronics	C'	7	
7	EE-3041	Propagation of Electromagnetic Waves and Antennas	C'	4	
8	EE-5022	Digital Signal Processing	E'	5	
9	EE-5042	Power Electronics	E'	7	
		<b>Total</b>		<b>56,50</b>	

**SPECIALISATION MODULES (SC)**

No.	Course ID	Course Title	Semester	ECTS Course Credits	Grades
1	EE-4021	Automatic Control Systems I	D'	4,5	
2	EE-5052	Automatic Control Systems II	E'	7	
3	EE-5011	Telecommunications I	E'	6	
4	EE-6032	Telecommunications II	F'	6	
5	EE-3051	Microprocessors I	C'	4	
6	EE-4012	Microprocessors II	D'	5,5	
7	EE-6061	Microprocessors Applications	F'	6	
8	EE-5031	Microwaves	E'	5	
9	EE-6021	Computer Networks	F'	6	
10	EE-7041	Telecommunication Networks	G'	5	
11	EE-6A41 ή EE-6B41	Industrial Automation or Optoelectronics & Optical Communications	F'	4	
12	EE-7A11 ή EE-7B11	Multimedia Applications or Internet Applications Programming	G'	4,5	

13	EE-7A21 ή EE-7B21	VLSI Circuit Design and Analysis or Radars and Electromagnetic Compatibility	G'	5	
14	EE-7A61 ή EE-7B61	Information Theory and Coding or Nanoelectronics	G'	4,5	
15	EE-6051	Wireless Links and Satellite Communications	F'	4	
16	EE-7031	Digital Television & Sound	G'	5	
17	EE-6011	Mobile Communications	F'	4	
18	EE-7071	Foreign Language Terminology	G'	3	
19	EE-8011	Dissertation(*)	H'	20	
20	EE-8021	Practical Training(**)	H'	10	(--) <sup>1</sup>
		<b>Total(According to student electives)</b>		<b>119,00</b>	

(\*) **Dissertation Title:** «.....»

(\*\*) **The Placement (6 months) took place in the enterprise / organisation**

«.....»

#### 4.4 Grading Scheme and if available, grade distribution guidance:

The grading scheme is based on the scale of ten as follows:

- 8.50 – 10.00: «Excellent»
- 6.50 – 8.49: «Very Good»
- 5.00 – 6.49: «Good»
- 4.00 – 4.99: «Insufficient»
- 0.00 – 3.99: «Fail»

The minimum pass mark is 5.0 (five). For more information: [www.teiath.gr](http://www.teiath.gr)

#### 4.5 Overall classification of the qualification(in original language):

“ \_\_\_\_\_ ”

### 5. INFORMATION ON THE FUNCTION OF THE QUALIFICATION

#### 5.1 Access to further study:

The degree of the Department gives access to postgraduate studies of MASTER's degree level.

#### 5.2 Professional status (if applicable):

The Department's graduate academic and professional title is «Electronics Engineer – Technological Education graduate». The professional rights of the Electronics Engineer - Technological Education graduate are stated in the Presidential Decree No. 346 / Official Gazette: FEK 158-14/06/1989. Upon completion of the programme of study, the graduate of

<sup>1</sup> indicative

the Department of Electronics has acquired knowledge and practical experience necessary to successfully serve the following fields, either on his own resources or in collaboration with other scientists and technologists:

1. Analog / digital electronic components, devices, systems and measurement instruments,
2. Telecommunication in general (wired / wireless, microwave – mobile – satellite - optical) as well as data communication,
3. Power electronics, Industrial electronics and automations, Automatic Control Systems, Mechatronic Systems,
4. Audio-visual systems and installations, Audio – video – TV broadcasting, Audio and Image / Multimedia Processing,
5. Computer technologies, Computer Networks, Internet and information services, Telematics applications,
6. Electromechanical structures, microsystems technology,
7. Microsensors, microprocessors, embedded systems,
8. Sensors, Measurement and data collection systems, Digital data processing,
9. Microelectronics, ICs and hardware structures,
10. Fire protection and safety systems, EMC, Quality assurance systems.

In these fields and in the corresponding sub-fields, the Department's graduate is able to undertake responsibilities as follows:

- i. Study, design, development, production, installation of electronic systems, devices or equipment, as listed above,
- ii. Operation, control, supervision, maintenance and repair of the equipment,
- iii. Organization and supervision of production, assembly or construction,
- iv. Technical / economo-technical studies, feasibility studies and expertise consultancy,
- v. Certification, quality control and EM compatibility of equipment and installations,
- vi. Organization and management of private enterprises,
- vii. Provision of technical services and consultancy services,
- viii. Design and implementation of applied research projects for the promotion of science and technology.

## 6. ADDITIONAL INFORMATION

### 6.1 Additional information:

- A. Moreover, the student has successfully attended the following free elective courses and has received the indicated grades:

#### FREE ELECTIVE COURSES

No.	Course ID	Module Title	Semester	ECTS Course Credits	Grades
1					
2					
3					
4					
5					
6					

7				
		<b>Total(According to student electives)</b>		

B. The following officially organized by the department of ..... seminars where attended by the graduate.

A/A	Seminar ID	Seminar Title
1		
2		
3		
4		

C. from .../.../..... to .../.../..... the graduate attended the course of [name the department] of [name the university- country] within the concept of LLP-ERASMUS. The modules successfully completed at the host institution are corresponded to EE-xxxx, EE-yyyy unit codes of the Department of Electronics. Grades and the respective ECTSs are given in the tables of §4.3 above.

**6.2 Further information sources:**

- Website of the Ministry of Education: [www.minedu.gov.gr](http://www.minedu.gov.gr)
- Website of TEI of Piraeus: [www.teiath.gr](http://www.teiath.gr)
- Website of the Department of Electronics Secretary: [www.teiath.gr/stef/electronics](http://www.teiath.gr/stef/electronics)
- Website of the Public Relations and Information Office: [career.teiath.gr](http://career.teiath.gr)

**Address**

TECHNOLOGICAL EDUCATION INSTITUTE (T.E.I.) OF ATHENS  
 AGIOU SPYRIDONOS ,GR-122 44, EGALEO – ATHENS, GREECE

<b>7.</b>	<b>CERTIFICATION OF THE SUPPLEMENT</b>
	Date:
The Secretary of Department	The Head of Department
THE PRESIDENT OF T.E.I. OF ATHENS	



## 8. INFORMATION ON THE NATIONAL HIGHER EDUCATION SYSTEM

### (i) Structure

According to the Framework Law (2007), higher education consists of two parallel sectors: the University sector (Universities, Polytechnics, Fine Arts Schools, the Open University) and the Technological sector (Technological Education Institutions (TEI) and the School of Pedagogic and Technological Education).

The same law regulates issues concerning governance of higher education along the general lines of increased participation, greater transparency, accountability and increased autonomy.

There are also State Non-university Tertiary Institutes offering vocationally oriented courses of shorter duration (2 to 3 years) which operate under the authority of other Ministries.

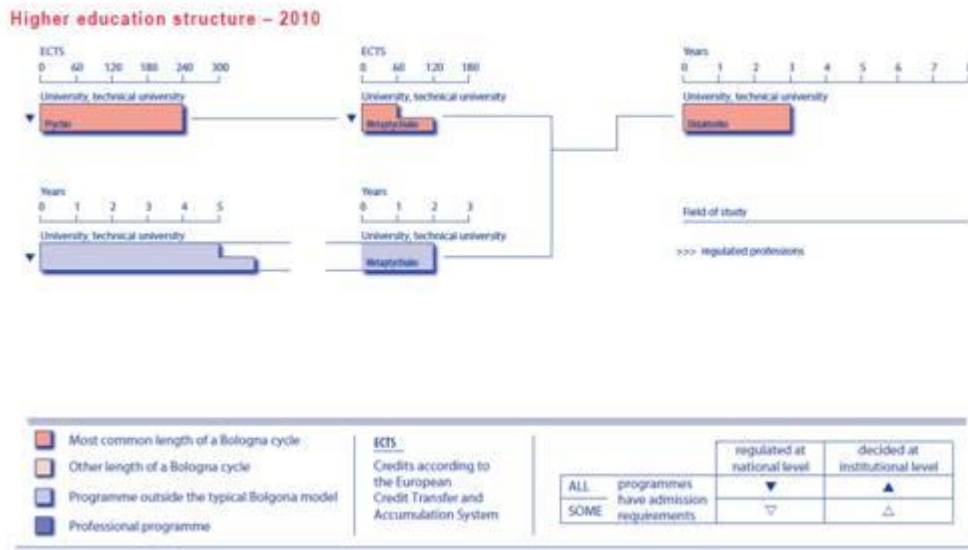
### (ii) Access

Entrance to the various Schools of the **Universities (*Panepistimio*)** and **Technological Education Institutions (*Technologiko Ekpaideftiko Idryma – TEI*)** depends on the general score obtained by Lyceum graduates on the Certificate, as described above (Section 5.iv), on the number of available places (*numerus clausus*) and on the candidates' ranked preferences among schools and sections.

### (iii) Qualifications

Students who successfully complete their studies in universities and TEI are awarded a *Ptychio* (first cycle degree). First cycle programmes last from four years for most fields to five years for engineering and certain other applied science fields and six years for medicine. The *Ptychio* leads to employment or further study at the post-graduate level that includes the one year second cycle leading to the second degree, *Metaptychiako Diploma Eidikefsis* – equivalent to the *Master's* degree – and the third cycle leading to the doctorate degree, *Didaktoriko Diploma*.

Recent legislation on quality assurance in Higher Education, the Credit Transfer System and the Diploma Supplement defines the framework and criteria for evaluation of university departments and for certification of student degrees. These measures aim at promoting student mobility and contributing to the creation of a European Higher Education Area.



<http://www.eurydice.org>

[http://www.eurydice.org/Eurybase/frameset\\_eurybase.html](http://www.eurydice.org/Eurybase/frameset_eurybase.html)